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LDaC: A Wideband Loran Data Acquisition System

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Goals

• Work for FAATC, subcontract through AMA



- Design/build wideband data acquisition system for Loran
 - >30kHz bandwidth
 - Multi-channel
 - Low cost
 - Open data format
 - System design freely available to Loran community

General Design





8th Order LP Filter Response



I&Q Demodulation then decimation





Down-conversion Optimization

- I&Q demodulation requires a large number of multiplications (2 per data sample) plus the calculation of the sine and cosine terms
- Low-pass filtering adds additional multiplications and additions (filter order multiplications and additions per data sample)
- Also, the filter has a delay equal to the number of taps or filter coefficients
- Make use of the following facts to reduce the number of calculations, reduce complexity, and improve performance
 - data will be decimated by a factor of 10 so only every 10th sample needs to be calculated
 - If the filter length is equal to the decimation then the filter delay products will not need to be saved due to the decimation
 - At IMHz sampling and 100kHz carrier, cosine, sine are periodic every 10 samples so only 10 sine, cosine coefficients are needed



9th Order FIR Filter Response



Filter magnitude response over -50 to 50 kHz region



Final System



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LDaC Software



File Format

Table I. LDaC File Format for One Channel of Data.

Channel	Data	Size	Туре
One	I	100,000	16 Bit Integer
One	Q	100,000	16 Bit Integer

Table 2. LDaC File Format for Two Channels of Data.

Channel	Data	Size	Туре
One	I	100,000	16 Bit Integer
One	Q	100,000	16 Bit Integer
Two	1	100,000	16 Bit Integer
Two	Q	100,000	16 Bit Integer

Table 3. LDaC File Format for Three Channels of Data.

Channel	Data	Size	Туре
One	I	100,000	16 Bit Integer
One	Q	100,000	16 Bit Integer
Тwo	I	100,000	16 Bit Integer
Тwo	Q	100,000	16 Bit Integer
Three	I	100,000	16 Bit Integer
Three	Q	100,000	16 Bit Integer

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Questions?

